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Attorney Docket No.: ELGP-6719-05
MAY 14 2009

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Assaf Shappir Examiner: Nguyen, Dang T
Serial No.: 10/537,857 Group Art Unit: 2824
Filed: 06/07/2005
Title: Method Circuit and System for Erasing One or More Non-Volatile
Memory Cells

RESPONSE

Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

Sir:

This communication is filed in response to an Office Action dated November 14th, 2008 issued by the United States Patent and Trademark Office in connection with the above-identified Application. A response to the Office Action was due January 14th, 2008. A Petition for a Three-Month Extension of Time is being filed herewith, along with the requisite fee. Accordingly, this communication is being timely filed.

Kindly amend the above-identified application and consider the remarks as follow:

Remarks/Arguments begin on page 2 of this paper.